

Fig. 1

FIG. 2 is a block diagram of a Packet Forwarding Engine 106, according to one embodiment of the present invention. The Packet Forwarding Engine 106 includes a Packet Processing block 207, a Forwarding Table (FT) block 116, and a plurality of Forwarding Path Controller (FPC) blocks 205. The Packet Processing block 207 is connected to the FT block 116 and to each of the FPC blocks 205. Each FPC block 205 is connected to a plurality of Packet Interface Controller (PIC) blocks 201. The Packet Forwarding Engine 106 is connected to a Router Element (RE) 105 and to a network of Packet In/Out.

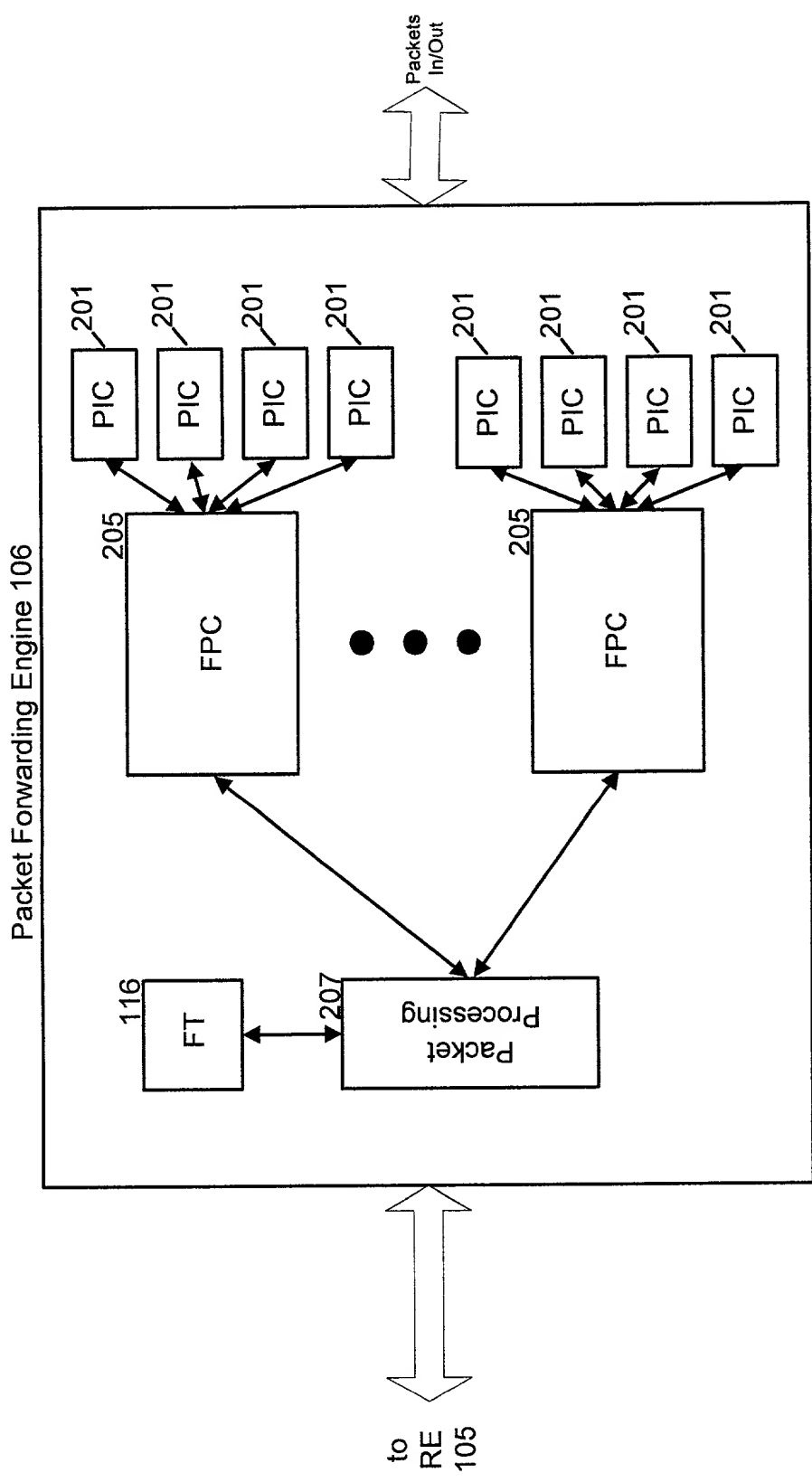


Fig. 2



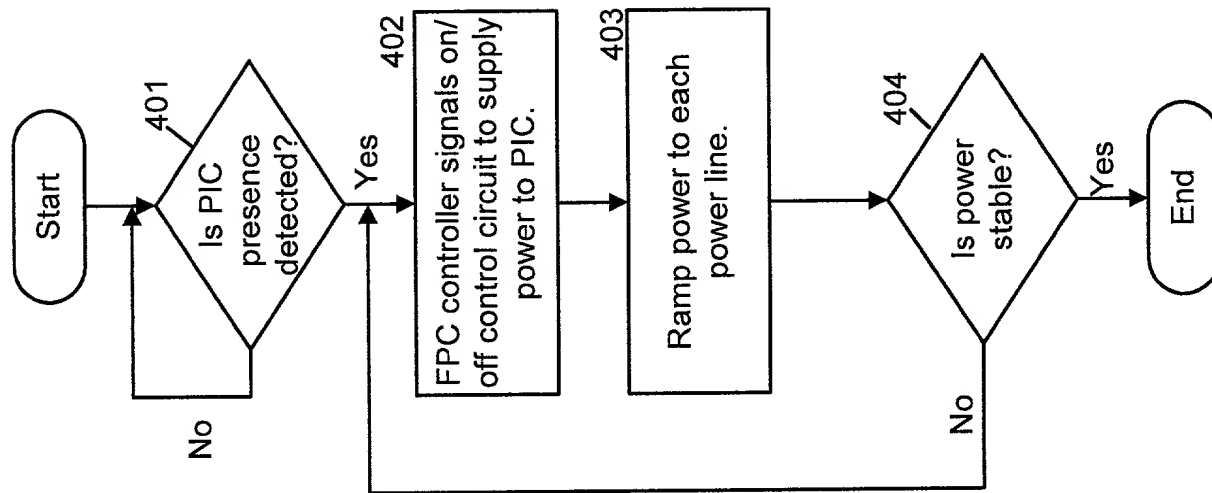


Fig. 4

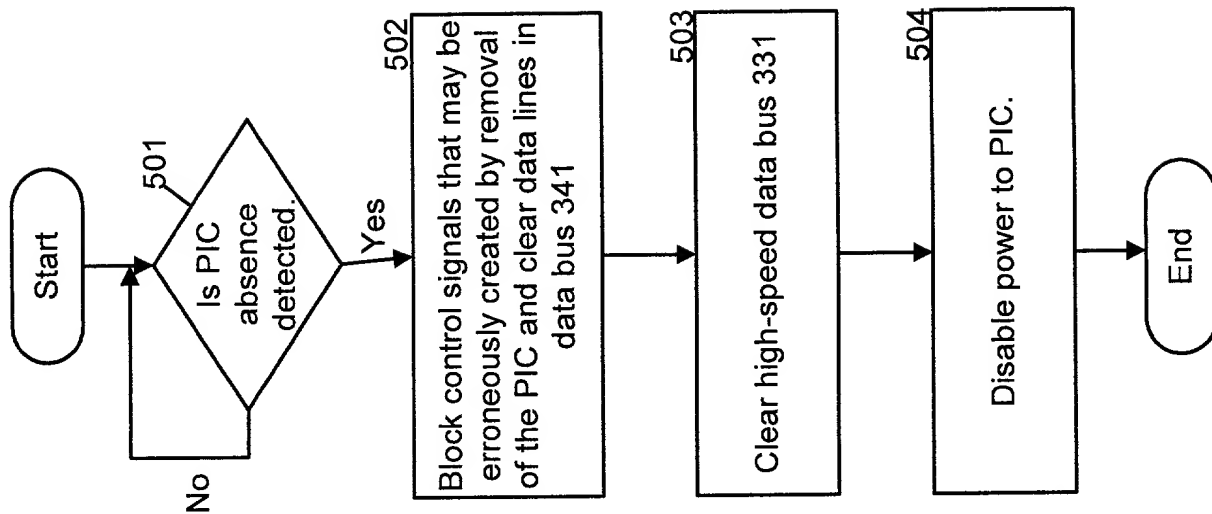


Fig. 5

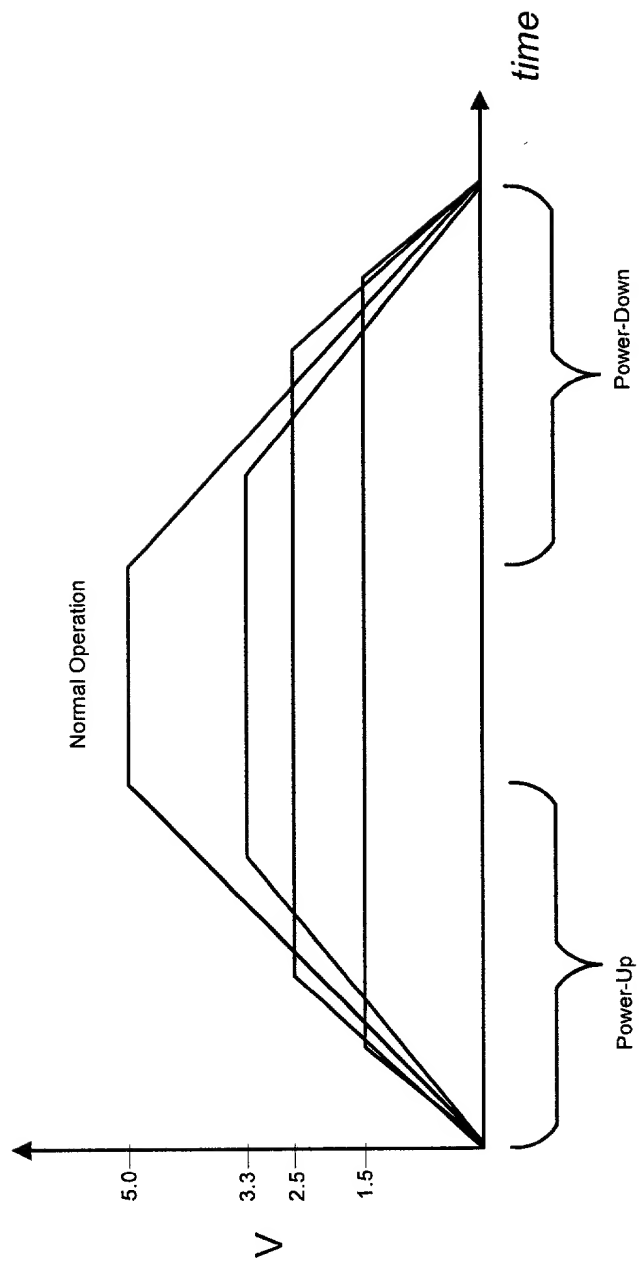


Fig. 6